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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/717,781
		Filing Date	November 20, 2003
		First Named Inventor	Martin Ostermayr
		Group Art Unit	
		Examiner Name	
Sheet	1	of	1
		Attorney Docket No.	LLP101US

#### U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
ap	AA	5,350,703		Lee	09/27/1994	Entire Document
	AB	5,471,416		Azmanov	11/28/1995	Entire Document
	AC	5,514,609		Chen et al.	05/07/1996	Entire Document
	AD	5,917,224		Zangara	06/29/1999	Entire Document
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	AG	6,107,666		Chang	08/22/2000	Entire Document
	AH	6,146,950		Sheu et al.	11/14/2000	Entire Document
	AI	2002/0042182	A1	Parris et al.	04/11/2002	Entire Document
ap	AJ	2002/0091894	A1	Garg	07/11/2002	Entire Document

#### FOREIGN PATENT DOCUMENTS

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
ap	BA	DE	42 26 421	A1	Samsung Electronics Co.	03/18/1993	Entire Document	✓
	BB	DE	100 05 460	A1	NEC Corp.	01/25/2001	Entire Document	✓
	BF	JP	4 - 276659				Entire Document	✓
ap	BG	JP	11-135651				Entire Document	✓

#### OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
ap	CA	"A New Contact Programming ROM Architecture for Digital Signal Processor", HIROSHI TAKAHASHI, SHIGETOSHI MURAMATSU and MASAYASU ITOIGAWA, 1998 Symposium on VLSI Circuits Digest of Technical Papers, IEEE, 1998, 5 pages.	
ap	CB	"An 80 ns 1 Mbit MASK ROM with a New Memory Cell", FUJIO MASUOKA, SHOJI ARIIZUMI, TAIRA IWASE, MICHIIRO ONO and NORIO ENDO, IEEE Journal of Solid-State Circuits, Volume SC-19, No. 5, October, 1984, 8 pgs.	
	CC		
	CD		
	CE		

Examiner Signature	<i>Chih Phung</i>	Date Considered	1/18/05
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